IN THE CLAIMS

Please cancel Claims 4 and 7-11 without prejudice and amend Claims

1, 2, 5, and 6 as shown in marked-up form:

1.   (Presently Amended) A method for testing digital circuitry

through effecting a paired loop-back from a first buffered output

to a first buffered input whilst within the circuitry executing at

least part of the test through using a Built-In-Self-Test

methodology,

        characterized by effecting said loop-back from the first

buffered data output to a buffered control input, wherein in

connection with said buffering, executing a conversion between a

digital full swing internal signal and an analog low swing external

signal and a conversion between an analog low swing signal and a

digital full swing signal, with respect to core circuitry of said

digital circuitry.

2.   (Presently Amended) A method for testing digital circuitry

through effecting a paired data loop-back from a first buffered

output to a first buffered input whilst within the circuitry

executing at least part of the test through using a Built-In-Self-

Test methodology,

characterized by effecting said loop-back from a buffered control output to the first buffered data input, _wherein in connection with said buffering, executing a conversion between a digital full swing internal signal and an analog low swing external signal and a conversion between an analog low swing signal and a digital full swing signal, with respect to core circuitry of said digital circuitry_.

3.   (Original) A method as claimed in Claim 1, characterized by effecting said loop-back from a buffered control output to the first buffered data input.

4.   (Withdrawn)

5.   (Presently Amended) A method as claimed in ~~Claims~~ _Claim_ 1, ~~whilst controlling~~_wherein_ both said loop-back as well as said buffering _are controlled_ through a one-bit control signal.

6.   (Presently Amended) A method as claimed in Claim 5, ~~whilst~~ _wherein_ ~~controlling~~ signal routing between said buffering on the one hand, and test circuitry as well as core circuitry of said digital circuitry, on the other hand, _are controlled_ through a plural bit control signal.

7-11 (Withdrawn)